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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	
		Filing Date	
		First Named Inventor	TERECHKO
		Art Unit	
		Examiner Name	
Sheet	1	of	1
		Attorney Docket Number	
		NL 030344	

Examiner Signature	/Keith Vicary/	Date Considered	11/30/2006
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
KV		"Exploring performance tradeoffs for clustered VLIW ASIPs" Margarida F. Jacome, et al.		
		"Application-specific Clustered VLIW Datapaths: Early Exploration on a Parameterized Design Space" Viktor S. Lapinskii, et al.		
		"High-Quality Operation Binding for Clustered VLIW Datapaths" Victor S. Lapinskii, et al.		
		"CALiBeR: A Software Pipelining Algorithm for Clustered Embedded VLIW Processor" Cagdas Akturan et al.		
↓		"Register File Port Requirements of Transport Triggered Architectures" Jan Hoogerbrugge et al.		
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